



**Programming for Opteron architectures**  
**David Cownie,**  
**HPC Benchmark Engineer**  
**AMD**

Mr. Cownie will be discussing AMD's Opteron(TM) processor architecture and how to effectively use it in technical computing. He will provide an overview of the innovations of the Opteron, compiler support for the Opteron and 64 bit extensions to the x86 architecture, as well as some future directions for the Opteron processor family.